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650 • 849 • 6600

Office of Initial Patent  
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Washington, DC 20231

U.S. Patent Application for:

Method Of Manufacturing Semiconductor Device and Manufacturing  
Apparatus

Inventors: Takayuki NIUYA et al.

Serial No.: 09/658,193

Filed: September 8, 2000

Sir:

Group Art Unit: 2812

Examiner:

REQUEST FOR CORRECTED FILING RECEIPT

We are forwarding herewith a copy of a filing receipt which has been received from the U.S. Patent and Trademark Office. There is an error in the filing receipt which is indicated in highlight on the copy. **The error is that the filing date is incorrect. The correct filing date is September 8, 2000.** The correct information is shown in red ink adjacent the erroneous information.

We request that a corrected Filing Receipt be supplied as soon as possible.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

By: David W. Hill

David W. Hill

Reg. No. 28,220

Dated:

Receipt  
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## UNITED STATES PATENT AND TRADEMARK OFFICE

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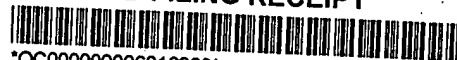
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/658,193	03/01/2001	2812	1240	08038.0032	8	10	2

09-08-00

CONFIRMATION NO. 2409

UPDATED FILING RECEIPT



\*OC000000006016309\*

 Finnegan Henderson Farabow  
 Garrett & Dunner LLP  
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 Washington, DC 20005-3315

Date Mailed: 04/30/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

 Takayuki Niuya, Tsukuba-Shi, JAPAN;  
 Michihiro Ono, Singapore, SINGAPORE;  
 Hideto Goto, Tsukuba-Shi, JAPAN;

## Domestic Priority data as claimed by applicant

THIS APPLICATION IS A CON OF PCT/JP00/03544 06/01/2000

## Foreign Applications

JAPAN 1999-154055 06/01/1999

If Required, Foreign Filing License Granted 10/23/2000

Projected Publication Date: 08/09/2001

Non-Publication Request: No

Early Publication Request: No

## Title

Method of manufacturing semiconductor device and manufacturing apparatus

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PLEASE STAMP TO ACKNOWLEDGE RECEIPT OF THE FOLLOWING:  
New U.S. Application for:

METHOD AND APPARATUS FOR PRODUCING SEMICONDUCTOR DEVICE  
being a Continuation of PCT International Application No. PCT/JP00/03544, filed June 1, 2000.

Inventor(s): Takayuki NIUYA, Michihiro ONO and Hideto GOTO

**BOX/PATENT APPLICATION**

1. Check for \$730.00
2. Transmittal Letter
3. Spec. 14 pgs. 1 indep. clm. and 10 clms. total
4. Drawings - 8 sheets of drawings containing 16 figures
5. Declaration and Power of Attorney
6. Recordation Form Cover Sheet
7. Assignment to TOKYO ELECTRON LIMITED

Dated: September 8, 2000  
CASE REF: 08038.0032  
DWH/FPD/rgm

(Due Date: February 1, 2001)



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Title	METHOD AND APPARATUS FOR ... DEVICE
Priority	JP PATENT APPLN. NO. 1999-154055 FILED ON JUN. 01, 1999